

REMARKS

Claims 1-40 are pending in the application, with claims 1-4, 6, 8, 9, 11-13, 15, 26-28, 32 and 33 rejected, claims 5, 7, 10, 14, 16, 17, and 34 objected to, claims 29-31 allowed, and claims 18-25 and 35-40 withdrawn from consideration.

Applicant thanks the Examiner for the indication of allowable subject matter in claims 5, 7, 10, 14, 16, 17, and 34, and the allowance of claims 29-31.

Applicant also thanks the Examiner for the courtesies extended to Applicant's representative during the telephone interview conducted on May 17, 2007. The substance of the interview is reflected in the Remarks below.

Claims 1-4, 6, 8, 9, 11-13, 15, and 26-28 remain rejected under 35 USC 103(a) as being unpatentable over Prestifilippo et al. (US Patent No. 5,446,889; hereinafter, "Prestifilippo") in view of Kodosky et al. (US Patent No. 6,608,638; hereinafter, "Kodosky"), and claims 32 and 33 remain rejected under 35 USC 103(a) as being unpatentable over Prestifilippo in view of Kodosky, and further in view of Chintalapati et al. (US Patent Appln. No. 2002/0120710; hereinafter, "Chintalapati"). Applicant continues to respectfully traverse these rejections for the reasons in the previous Response. For the Examiner's convenience, these reasons are repeated hereinbelow.

Neither Prestifilippo nor Kodosky, alone or in combination, suggests a method of operating hardware resources in a wireless communication device, as required by the claimed invention. Prestifilippo is instead directed generally to a linked-list method, and Kodosky is directed generally to a computer-implemented system and method for generating a hardware implementation of graphical code. There is no logical basis for combining these references in the context of operating resources of a wireless communication device. Thus, the claims are patentable over the applied references for at least this reason.

Additionally, the applied references do not suggest performing in real time while the wireless communication device is operating, as also required by the claimed invention. While the

portion of Kodosky to which the Examiner refers mentions "real time," it is in the context of a bus routing timing and trigger signals while creating the hardware element. The claimed invention, on the other hand, is designed for real time coordinating, via scheduling and allocation, a set of hardware resources. Thus, while Kodosky does discuss a real time bus routing function during hardware *creation*, it does not suggest real time *operation* of hardware, let alone a wireless communication device. Thus, the claimed invention is patentable over the applied references for this additional reason.

During the interview the Examiner acknowledged that the applied references do not teach or suggest a method of operating hardware resources in a wireless communication device. Following the interview the Examiner "performed a very quick search," and in the Interview Summary identified Storino et al. (US 2002/0115428) and Koplar et al. (US 2002/0112250).

Neither Storino et al. nor Koplar et al. has been applied against the claims. In any event, Applicant notes that neither of these references teaches or suggests a "first address containing operating information associated with a first hardware resource," as required by claims 1-17 and 26, "transmitting control information associated with [a] first memory address," as required by claim 27, "means for locating a first memory address in the memory associated with a first hardware resource," as required by claim 28, "locating a current address in a memory, the current address containing operating information associated with a current hardware resource of the plurality of hardware resources," as required by claims 29 and 31, "locating a current address in a memory, the current address containing operating information associated with a current hardware resource of the plurality of hardware resources," as required by claim 30, "means for locating a current address in the memory, the current address containing operating information associated with a current hardware resource," as required by claims 32 and 34, or "means for locating a current address in the memory, the current address containing operating information associated with a current hardware resource," as required by claim 33.

In view of the above, Applicant believes the pending application is in condition for allowance.

In the event a fee is required or if any additional fee during the prosecution of this application is not paid, the Patent Office is authorized to charge the underpayment to Deposit Account No. 50-2215.

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Respectfully submitted,

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